# DECLARATION (37 CFR 1.63) FOR UTILITY OR DESIGN APPLICATION USING AN APPLICATION DATA SHEET (37 CFR 1.76)

Electronic Version v11
Stylesheet Version v10

Title of Invention

SENSING CIRCUIT FOR SINGLE BIT-LINE SEMICONDUCTOR MEMORY DEVICE

As the below named inventor, I declare that:

This declaration is directed to the invention titled: "SENSING CIRCUIT FOR SINGLE BIT-LINE SEMICONDUCTOR MEMORY DEVICE"

I believe that I am the original and first inventor of the subject matter which is claimed and for which a patent is sought;

I have reviewed and understand the contents of the above-identified application, including the claims, as amended by any amendment specifically referred to above;

I acknowledge the duty to disclose to the United States Patent and Trademark Office all information known to me to be material to patentability as defined in 37 CFR 1.56, including for continuation-in-part applications, material information which became available between the filing date of the prior application and the national or PCT International filing date of the continuation-in-part application.

All statements made herein of my knowledge are true, all statements made herein on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like are punishable by fine or imprisonment, or both, under 18 U.S.C. 1001, and may jeopardize the validity of the application or any patent issuing thereon.

#### **FULL NAME OF INVENTOR:**

Inventor: Shih-Huang Huang	Inventor
Signature :	Citizen of : TW

#### **DECLARATION**

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment or both under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued hereon.

Post Office Address: and Residence Citizen of:

Shih-Huang Huang

No. 22, Lane 210, Tung-Nan St., Hsin-Chu City, Taiwan, R.O.C.

R.O.C.

### COMBINED DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I hereby declare that:

I believe I am the sole (if only one name appears below), or joint (if more than one name appears), original and first inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled:

## "SENSING CIRCUIT FOR SINGLE BIT-LINE SEMICONDUCTOR MEMORY DEVICE"

+ The specification for th	e above entitled invention	is filed herewith.
The specification for the above entitled invention was filed previously		
with application serial number:	Filing	Date:
I hereby state that I have specification, including the claim		contents of the above identified tent referred to above.
I acknowledge the duty to the invention disclosed in this Regulations, Section 1.56 (a). I application to disclose to the Pater to the patentability of the invent became available to me between the application.	application in accordance wi further acknowledge the du nt and Trademark Office all int ion disclosed in this applicat:	ty in any continuation-in-part formation known to be material ion, as defined in 1.56, which
	PRIORITY CLAIM	1
There is no claim of prior	rity	
	Taiwan R.O.	114801490: D.
PO	WER OF ATTORNI	<u>EY</u>
As a named inventor, application and to transact all rele Winston Hsu Mord Michael Lewis	I hereby appoint the following ated business in the Patent and Registration# 41,526 Registration# 50,478	
P.O. Box 506, Merrifield, Virginia 2211	6,	

U.S.A

TEL: (02)8923-7350